

Fig. 1

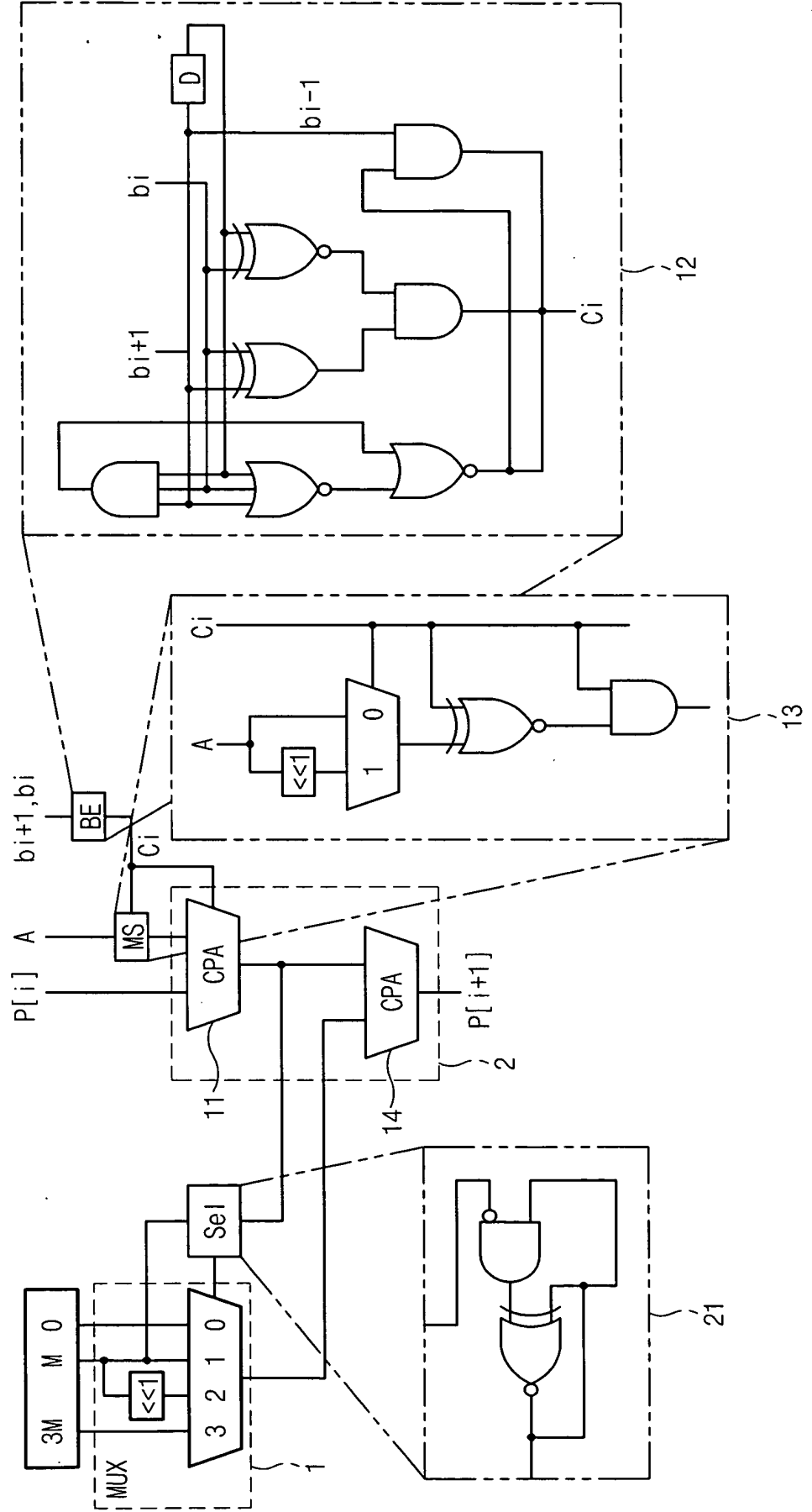


Fig. 2

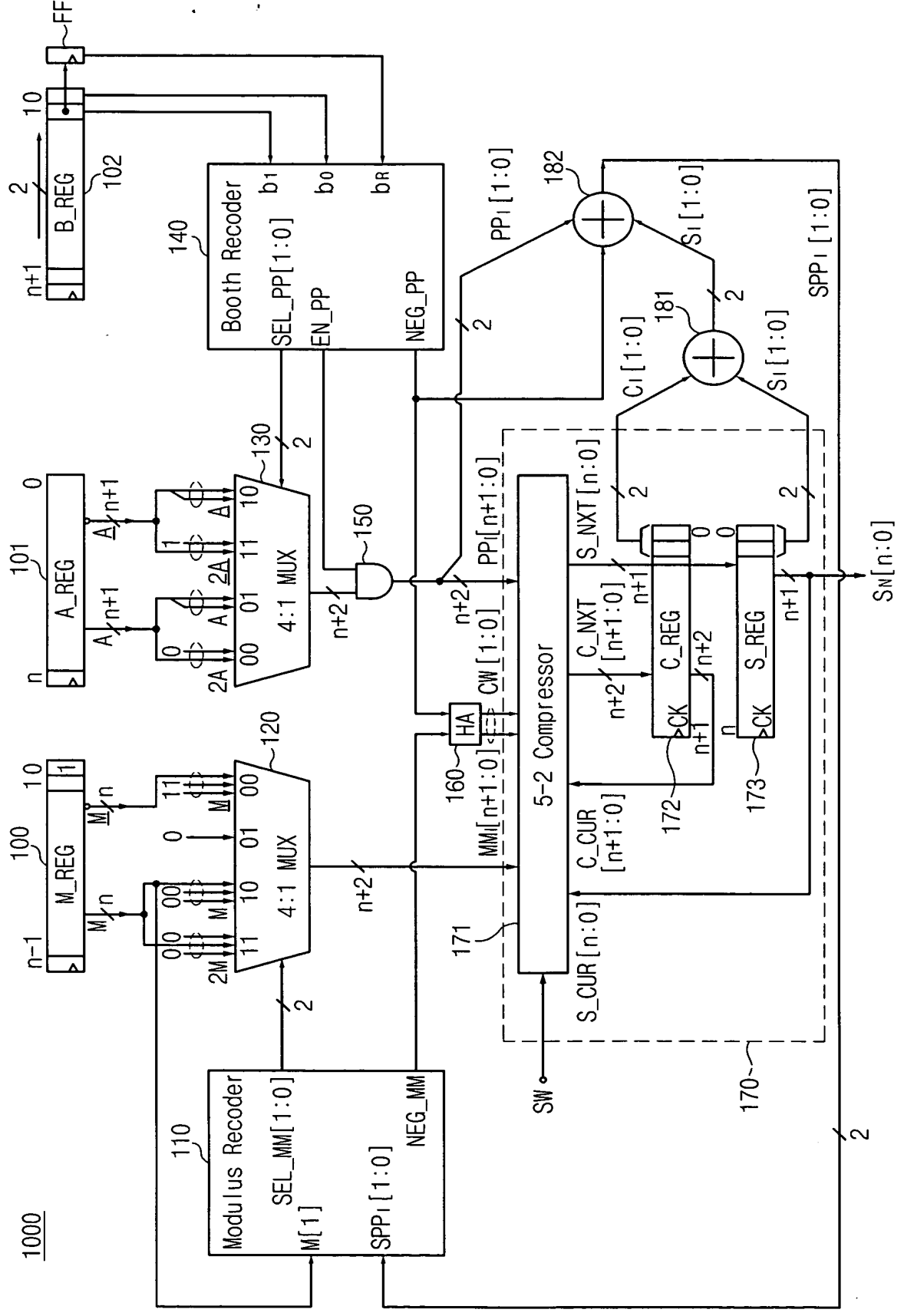


Fig. 3

Inputs of Modulus Recoder(110)			Outputs of Modulus Recoder(110)		Selected MM ₁ [n+1:0]
SPP ₁ [1]	SPP ₁ [0]	M[1]	SEL_MM[1:0]	NEG_MM	
0	0	0	11	0	0
0	0	1	11	0	0
0	1	0	01	1	\overline{M}
0	1	1	00	0	M
1	0	0	10	0	2M
1	0	1	10	0	2M
1	1	0	00	0	M
1	1	1	01	1	\overline{M}

Fig. 4

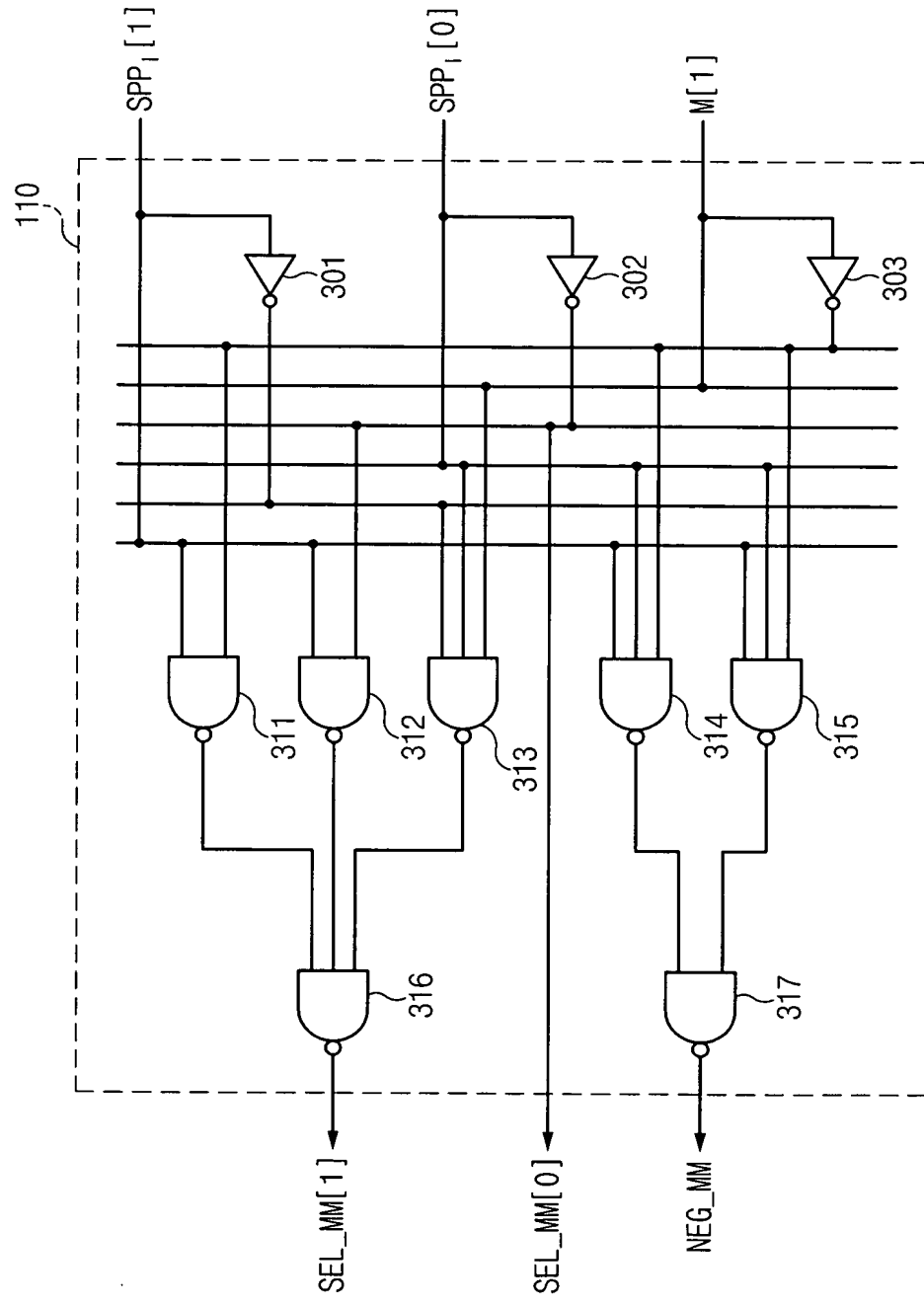


Fig. 5

Inputs of Booth Recoder(140)			Outputs of Booth Recoder(140)			Selected PP ₁ [n+1:0]
b ₁	b ₀	b _R	SEL_PP[1:0]	EN_PP	NEG_PP	
0	0	0	Don't care	0	0	0
0	0	1	00	1	0	A
0	1	0	00	1	0	A
0	1	1	10	1	0	2A
1	0	0	11	1	1	<u>2A</u>
1	0	1	01	1	1	<u>A</u>
1	1	0	01	1	1	<u>A</u>
1	1	1	Don't care	0	0	0

Fig. 6

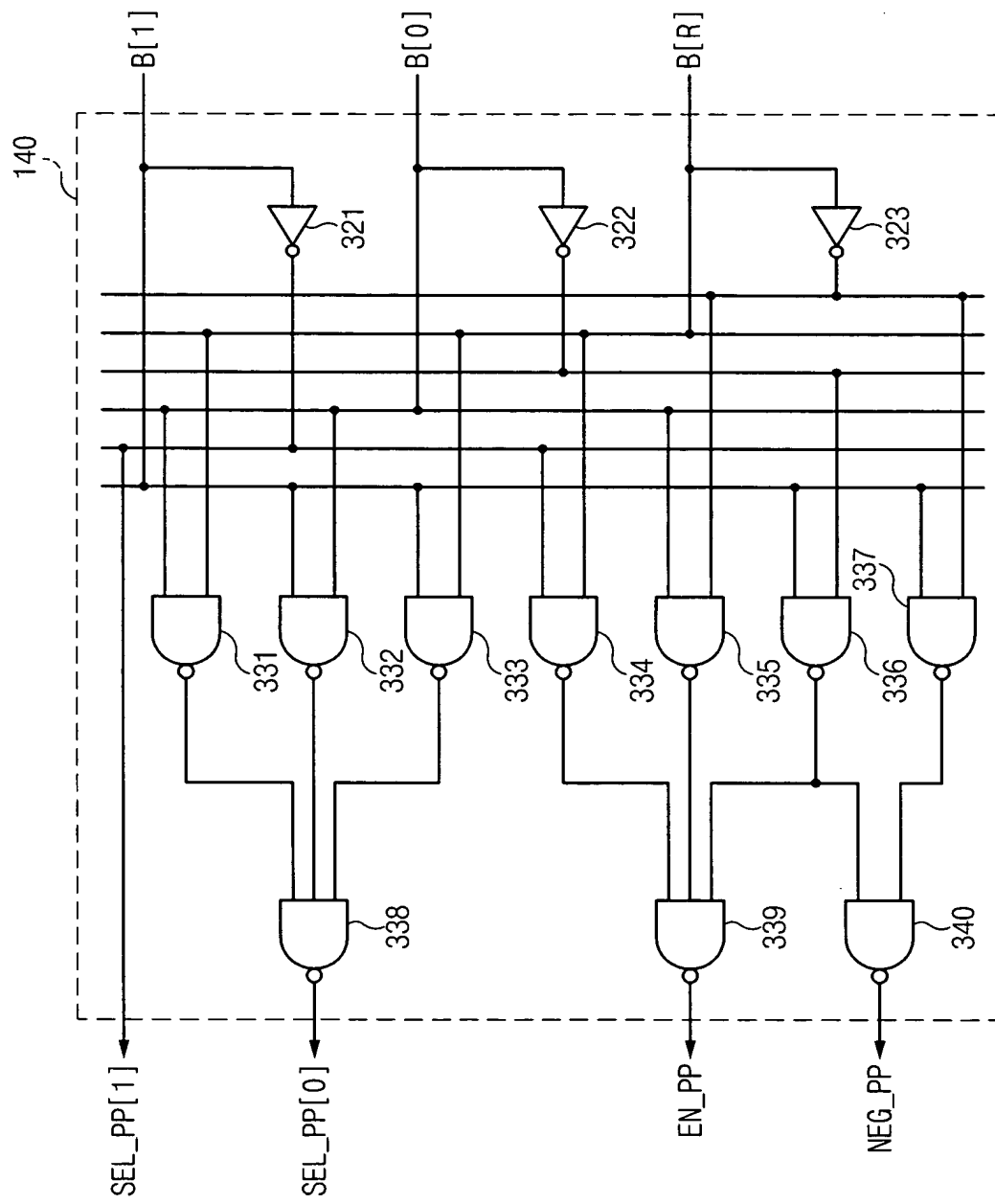


Fig. 7

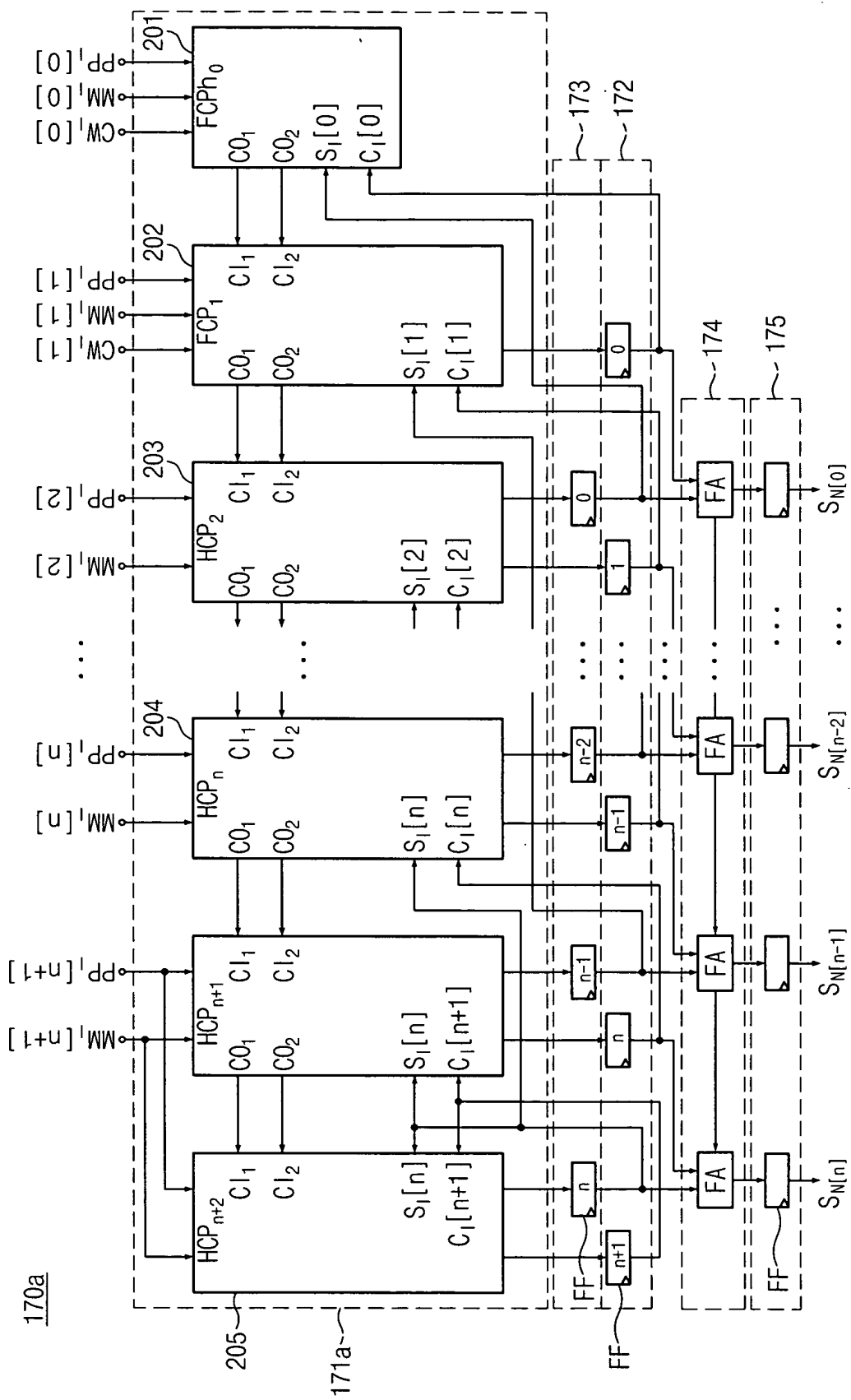


Fig. 8

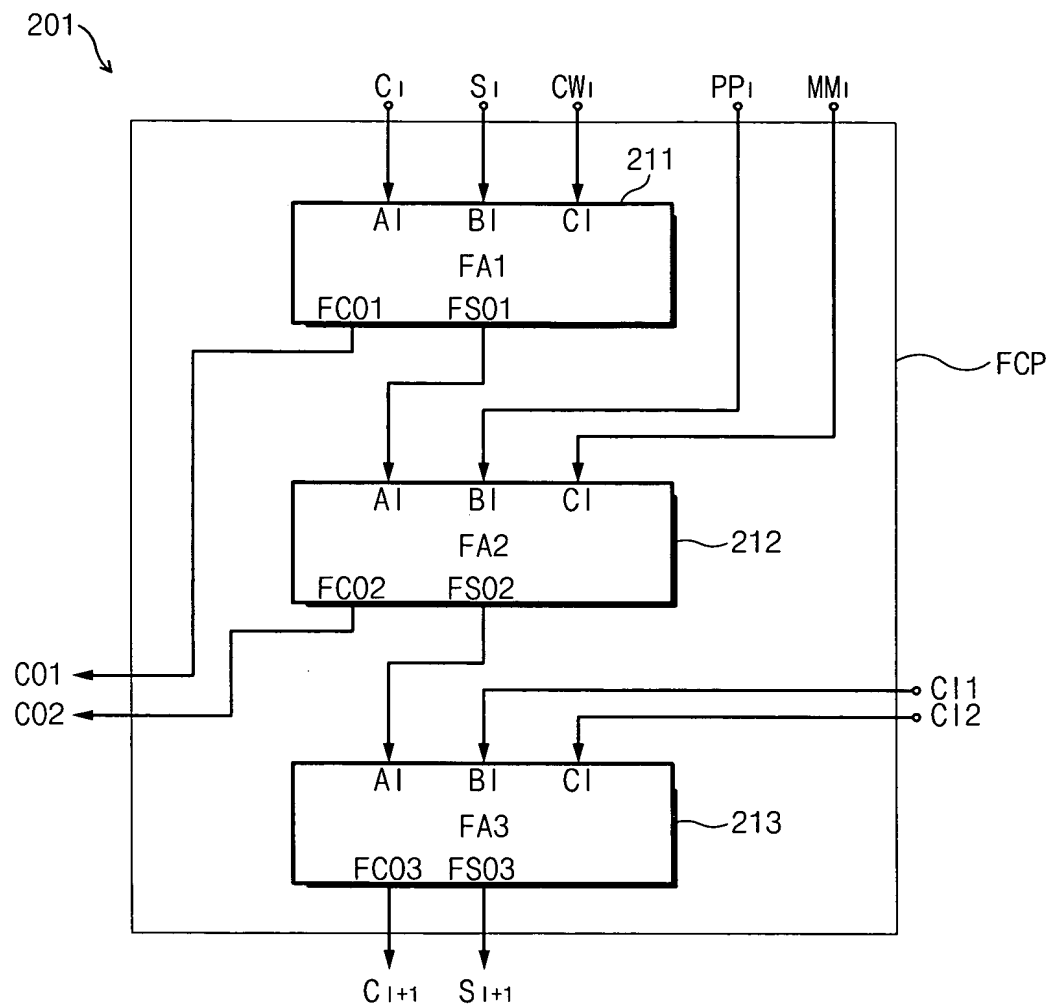


Fig. 9

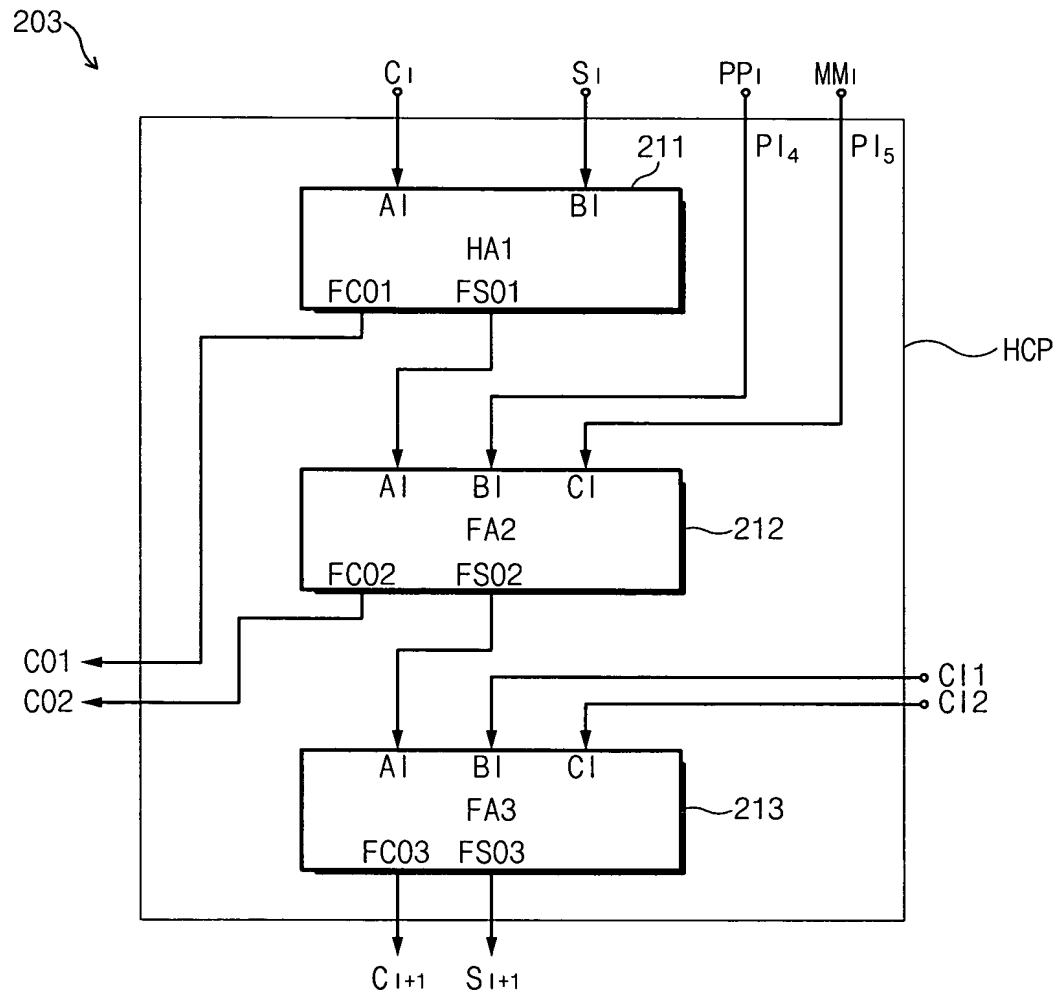


Fig. 10

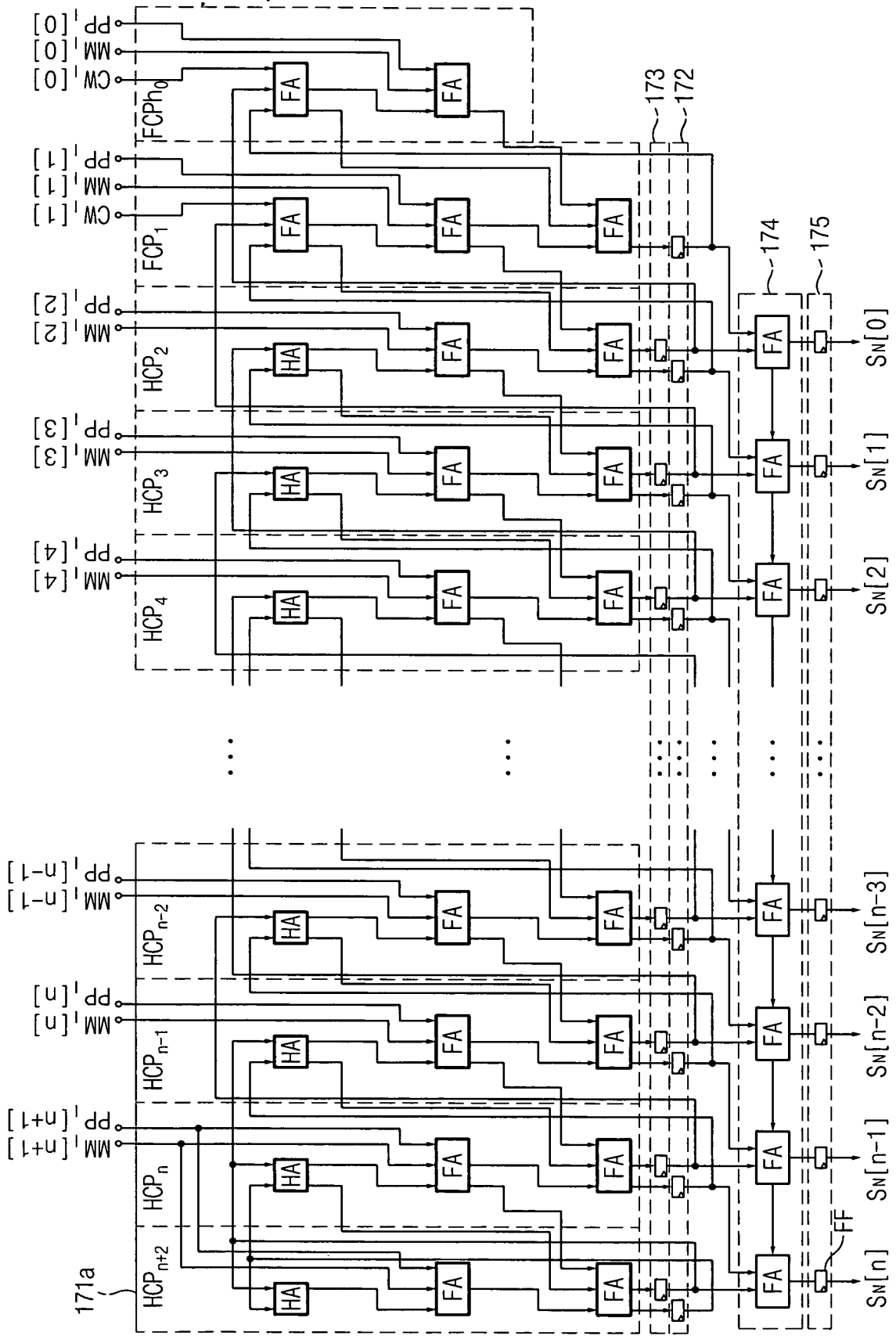


Fig. 11

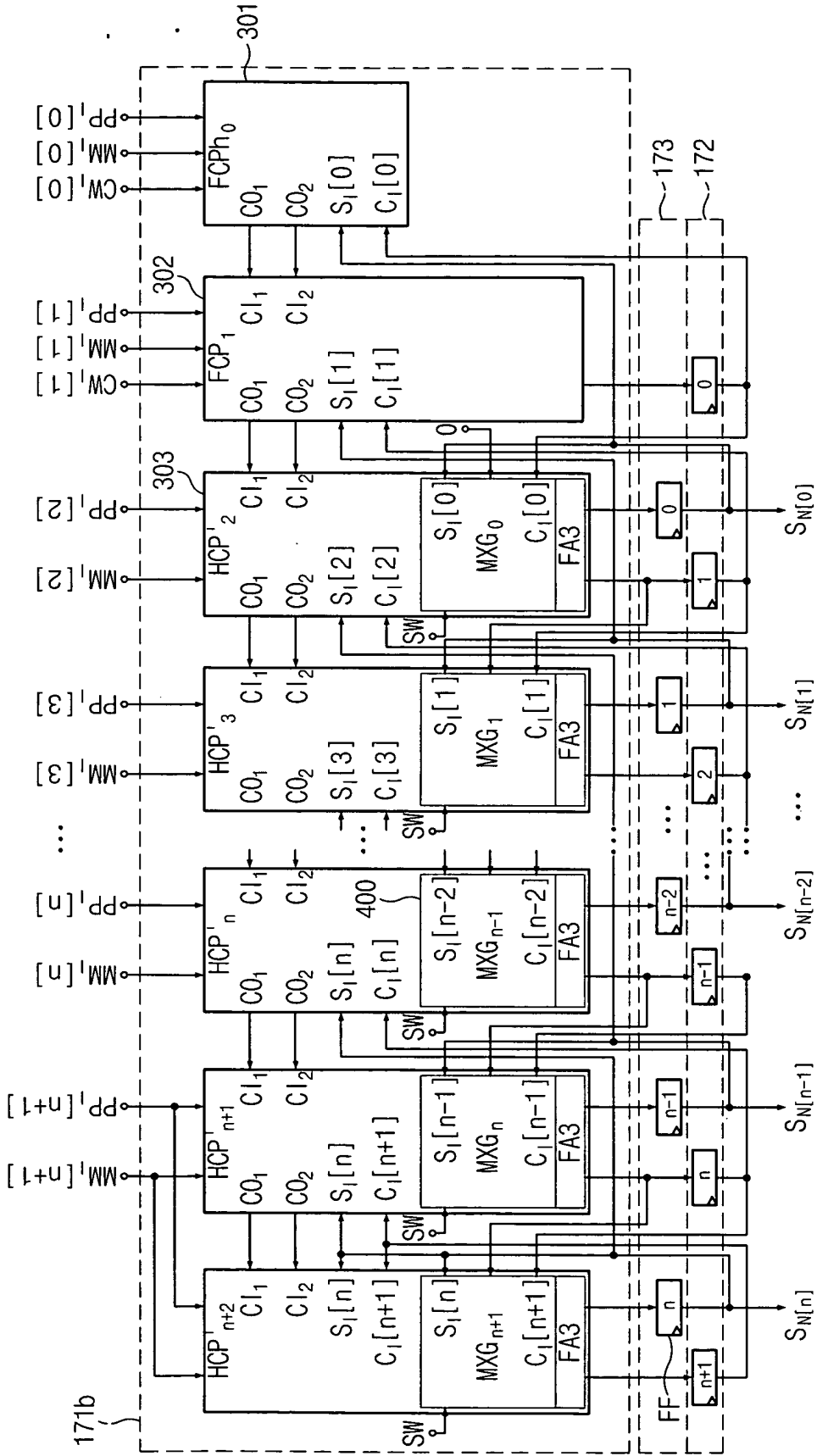


Fig. 12

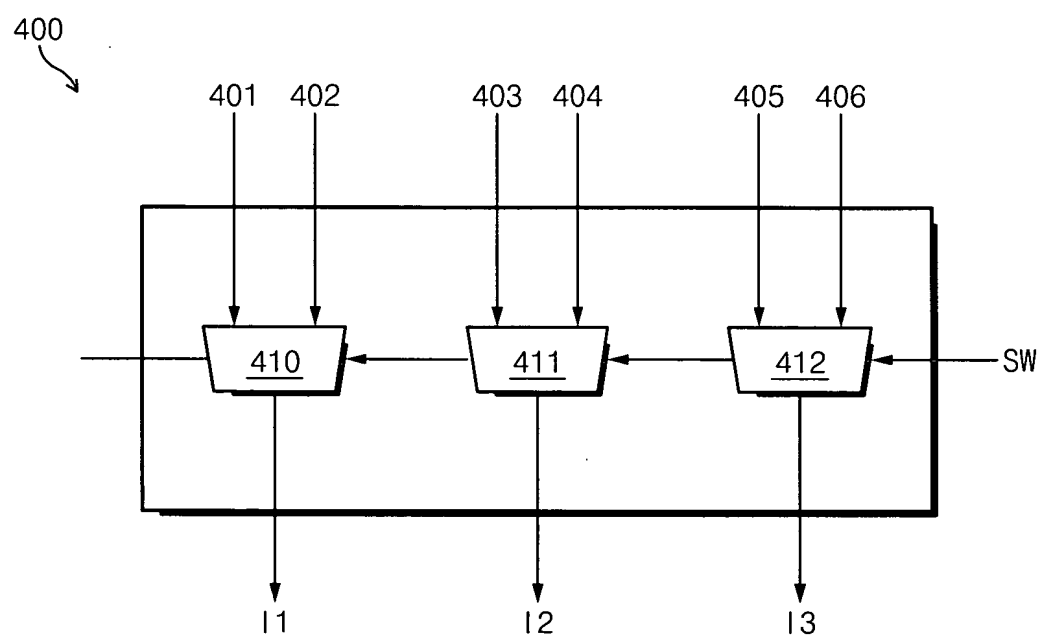


Fig. 13

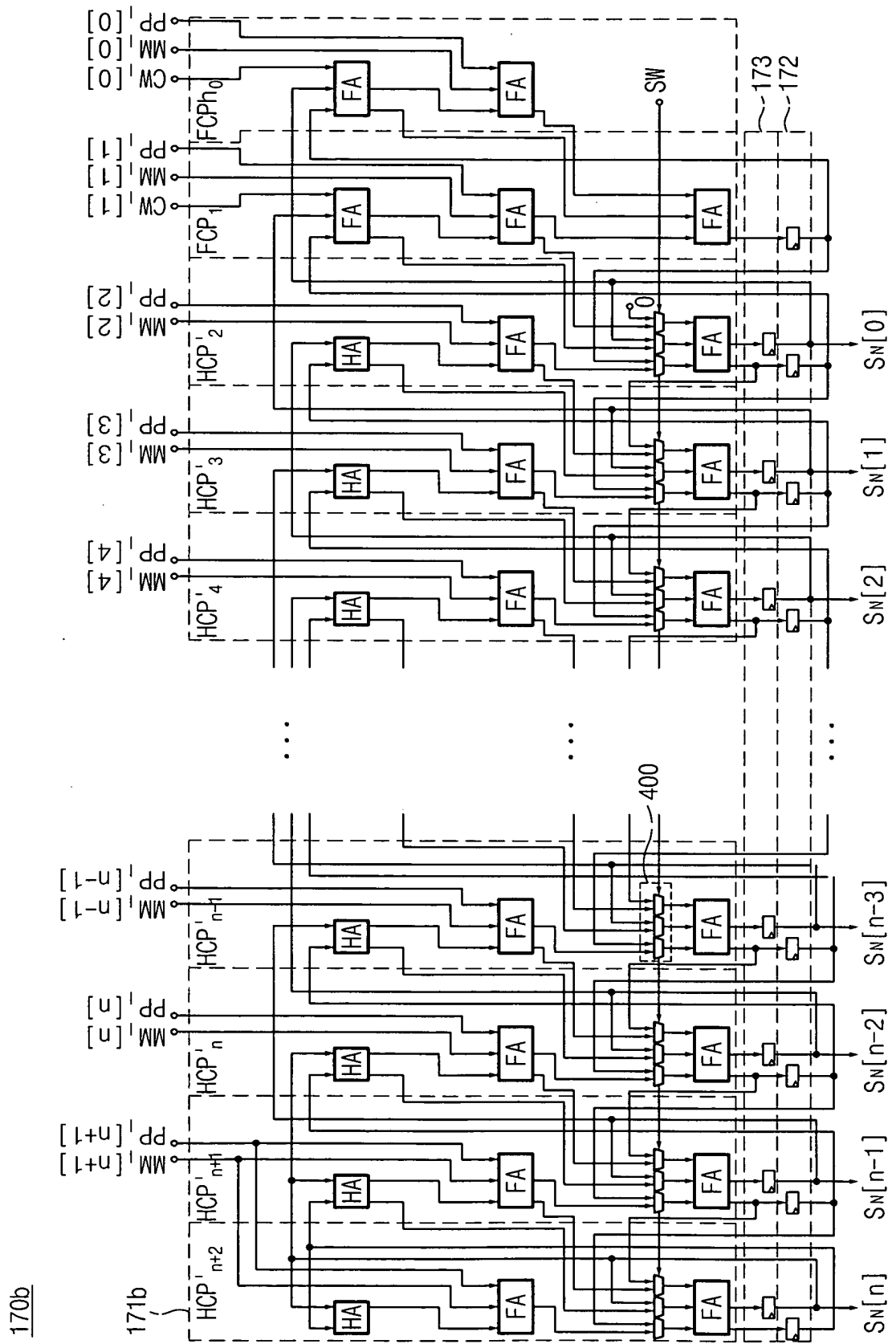


Fig. 14

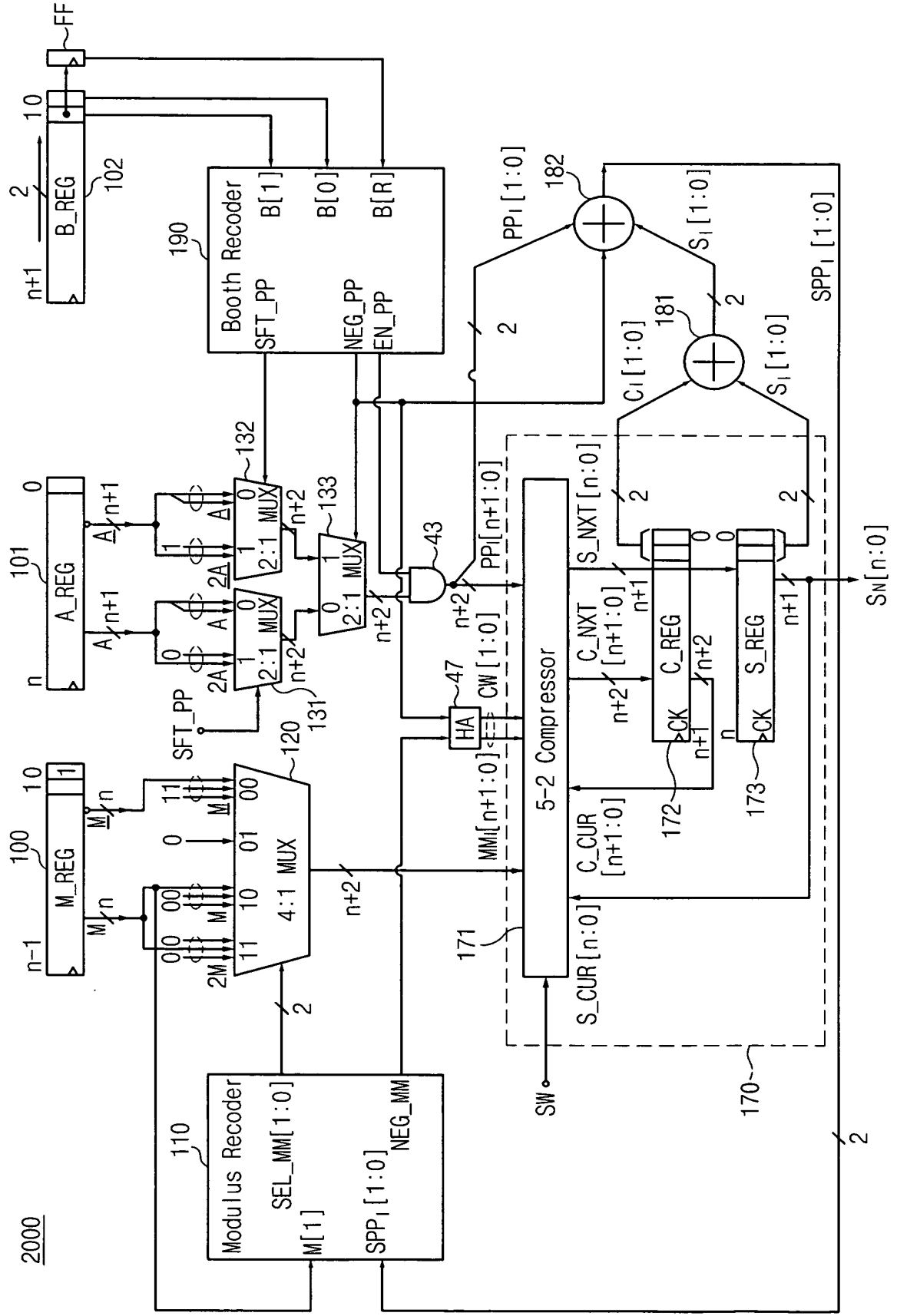


Fig. 15

Inputs of Booth Recoder(190)			Outputs of Booth Recoder(190)			Selected PP _i [n+1:0]
B[1]	B[0]	B[R]	SFT_PP	EN_PP	NEG_PP	
0	0	0	0	0	0	0
0	0	1	0	1	0	A
0	1	0	0	1	0	A
0	1	1	1	1	0	2A
1	0	0	1	1	1	<u>2A</u>
1	0	1	0	1	1	<u>A</u>
1	1	0	0	1	1	<u>A</u>
1	1	1	0	0	0	0

Fig. 16

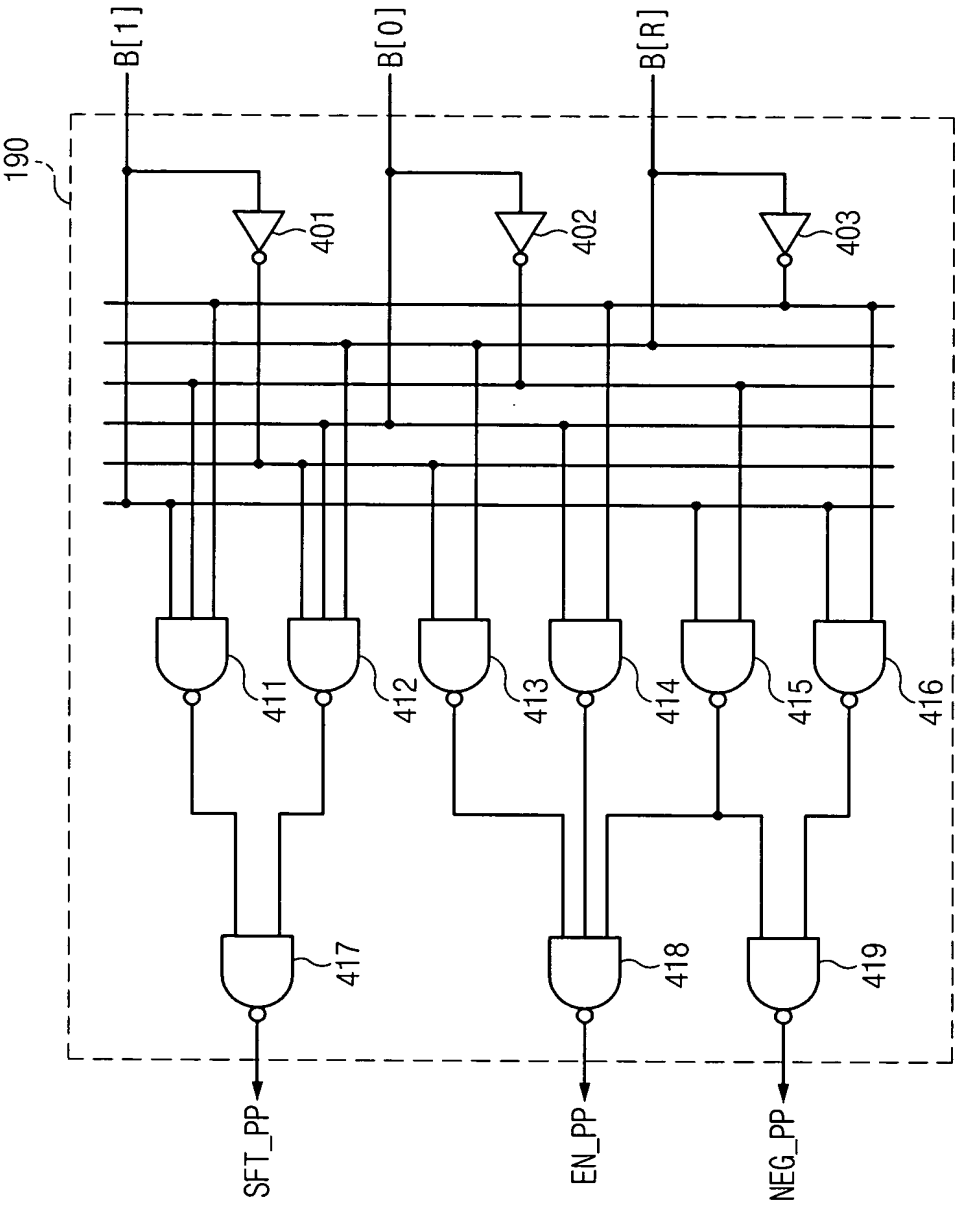


Fig. 17

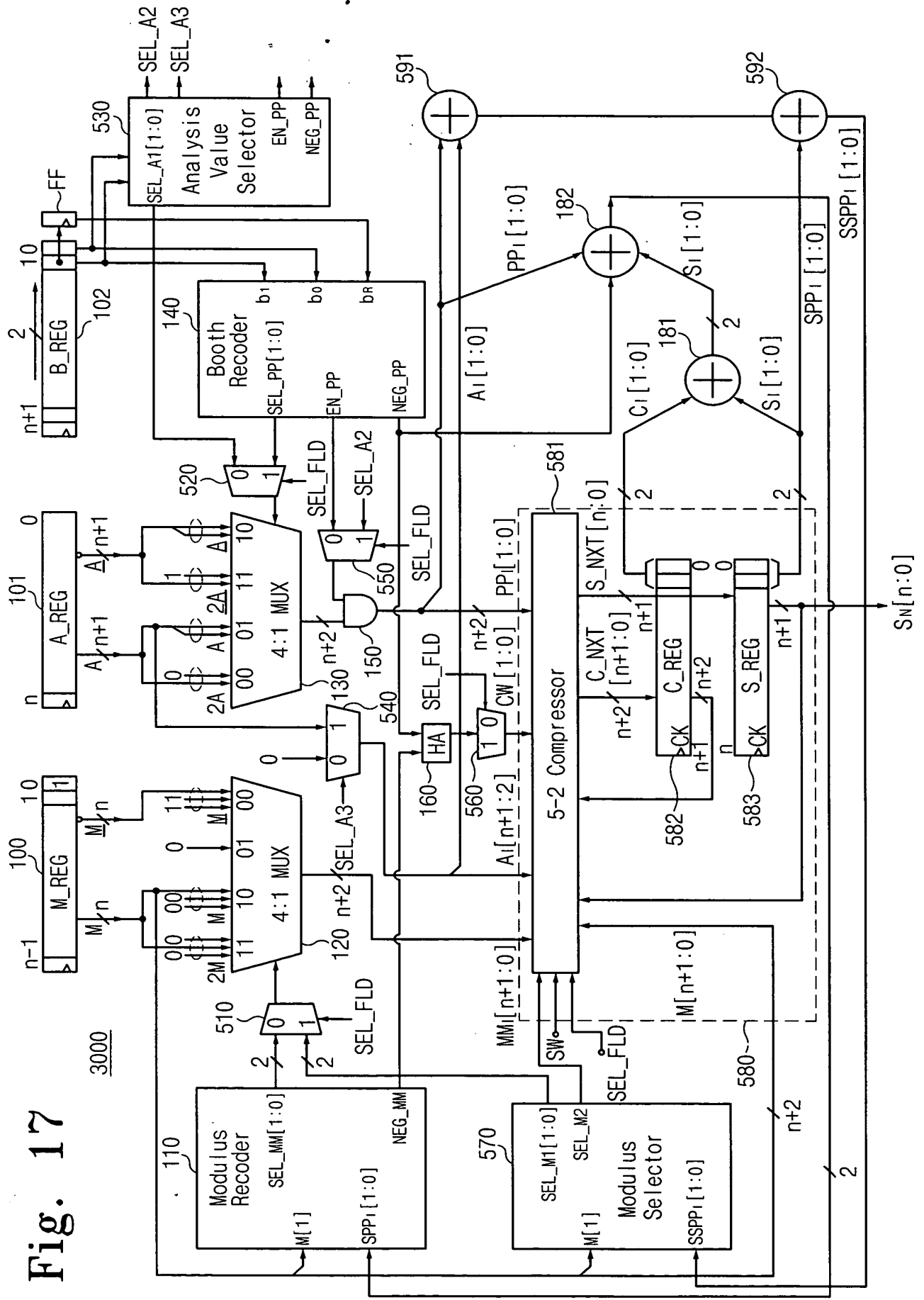


Fig. 18

Inputs of Analysis Value Selector (530)		Outputs of Analysis Value Selector (530)			Selected First Analysis Value $PP_1[n+1:0]$	Selected Second Analysis Value $A_1[n+1:0]$
B[0]	B[1]	SEL_A1[1:0]	SEL_A2	SEL_A3		
0	0	Don't care	0	0	0	0
0	1	01	1	0	A	0
1	0	00	1	0	2A	0
1	1	00	1	1	2A	A

Fig. 19

Inputs of Modulus Selector (570)		q_i	Outputs of Modulus Selector (570)		Selected First Analysis Value $MM_i[n+1:0]$	Selected Second Analysis Value $M_i[n+1:0]$
$SSPP_i[1:0]$	$M[1]$		$SEL_M1[1:0]$	SEL_M2		
00	0	00	01	0	0	0
01	0	01	10	0	M	0
10	0	10	11	0	2M	0
11	0	11	11	1	2M	M
00	1	00	01	0	0	0
01	1	11	11	1	2M	M
10	1	10	11	0	2M	0
11	1	01	10	0	M	0

Fig. 20

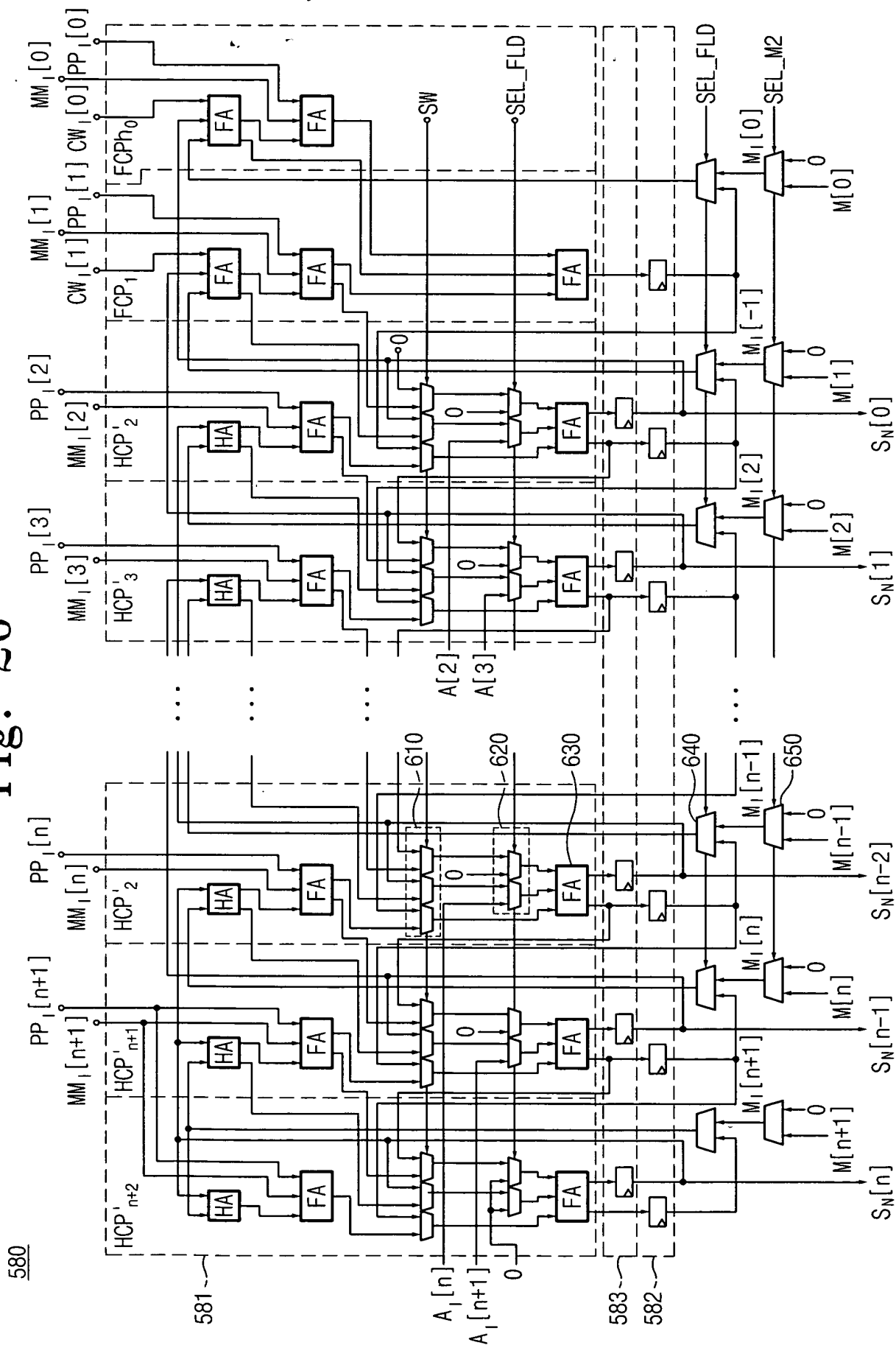


Fig. 21

